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*Curriculum Vitae*

muhammad aqeel aslam

**Objective**

**To get an attractive job in a well reputed organization that will effectively utilize my academic, technical, management and communication skills.**

**Expereince**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Sr No.** | **Organization** | **Position** | **Joining Date** | **Leaving Date** | **Reason for Leaving** |
| 1. | Swedish College of Engineering & Technology, Raim Yar Khan | Lecturer | Nov, 2012 | Up to date | NA |
| 2. | The Resource Group (TRG) | CSE | Oct,2011 | Aug, 2012 | NA |
| 3. | Govt. College of Technology, S.I.T.E, Karachi | Lecturer (Visiting Faculty) | Oct,2008 | Dec,2009 | NA |
| 4. | KMTC, CDGK  | Computer Analyst | July, 2007 | July, 2008 | NA |

**Education**

* **2012** M. S. Electrical Engineering (Specialization in Communication)

Pakistan Navy Engineering College, Karachi

National University of Science &Technology, Pakistan

C.G.P.A. 3.16 on the scale of 4.00

* **2007** B. S. Electronic Engineering

Sir Syed University of Engineering & Technology, Karachi, Pakistan

C.G.P.A. 3.00on the scale of 4.00 (First Class)

* *Higher Secondary &Secondary School Certificates*

Grades B & A respectively

**Projects & thesis**

1. **Secure Implementation of RSA Algorithm Against Timing Attacks** (Master’s Thesis)
2. **Remote Telemetry and Data acquisition using 2.4 GHz RF Communication link** (Bachelor’s Project)

**Minor Projects**

1. Digital Clock with Alarm using Verilog
2. Speed control of D.C motor using Operational Amplifier
3. R/C car control via parallel port (JAVA)
4. RPM counter & serial port interfacing (c51 micro-controller)
5. Constructed a Digital clock with the help of various IC’s
6. Seven Segment Display interfacing with PC through parallel port using C-language
7. Constructed a water level detector circuit with the help of a sensor and a buzzer

**Current projects**

1. *In this semester I am teaching in SCET, Rahim Yar Khan, Robotics and FPGA Based Design System*
2. *In last semester I taught, Signals and Systems*

**SKILLS**

1. *Computer Literacy:* Windows, Microsoft Office, General Purpose Software

*Programming Languages*: C, Assembly Language, Verilog HDL, JAVA

*Engineering Analytical Tools:* MATLAB, Mathematica, PSPICE, Perl, Wireshark, MRTG ,Perl, Xilinx

1. Introduction with PLC, LabVIEW, SCADA,, WIFI, VSATS
2. *Linguistics:* English (Fluent), Urdu (Fluent), French (Beginner)

**ACtivities & achievements**

1. Enrolled in MS Leading to PhD from National University of Sciences and Technology, Islamabad
2. Scholarship for the Completion of BS Electronic Engineering at Sir Syed University of Engineering & Technology, Karachi, Pakistan
3. Internship in Avionics Department of Pakistan International Airlines.

**personal DETAILS**

*Date of Birth:* 16th January, 1985

*Gender:* Male

*Nationality:* Pakistani

1. *CNIC No.*42201-6449207-9